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PATENT  
Attorney Docket No.: 020408-001500US  
Client Ref. No.: PN017

at Fax No.: 1-703-872-9318

On May 17, 2002

TOWNSEND and TOWNSEND and CREW LLP

By: J. Maitre-jun

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Arnold R. Feldman

Application No.: 09/835,021

Filed: April 13, 2001

For: LOW POWER LARGE SIGNAL RF  
TUNED BUFFER AMPLIFIER

Examiner: Terry Lee England

Art Unit: 2816

AMENDMENT

FAX COPY RECEIVED

MAY 18 2002

TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the office action mailed December 17, 2001, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please replace the three paragraphs starting on page 3 line 28 and ending on page 4 line 22 with the following three paragraphs:

Figure 1 is a schematic for a conventional RF buffer 100 used in wireless circuits. Included are differential pair M10 130 and M20 140, inductive loads L1 110 and L2 120, and current source 150. Current source 150 provides a bias current for the buffer. A first supply voltage VDD is applied on line 160, and a second supply voltage VSS is applied on line 155. An input voltage Vin between lines 135 and 145 is applied to